

UT32M0R500 Unused Pin Termination

UT32M0R500 32-bit Arm[®] Cortex[®] M0+ Microcontroller

Application Note

Cobham.com/HiRel

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The most important thing we build is trust

Table 1: Cross Reference of Applicable Products

PRODUCT NAME	MANUFACTURER PART NUMBER	SMD #	DEVICE TYPE	INTERNAL PIC NUMBER
Arm Cortex M0+	UT32M0R500	5962-17212	---	QS30

1.0 OVERVIEW

The following is a pin list for termination connection for unused pins. The pin state is assumed to be immediately after a power-on or reset of the UT32M0R500.

Pin No.	NAME	RESET STATE	Recommended Termination when Unused
E12	AIN0	-	Pulldown to 0V or VSSA
F12	AIN1	-	
G12	AIN2	-	
H12	AIN3	-	
F11	AIN4	-	
G11	AIN5	-	
H11	AIN6	-	
J11	AIN7	-	
J12	AIN8	-	
K12	AIN9	-	

Pin No.	NAME	RESET STATE	Recommended Termination when Unused
L12	AIN10	-	
M12	AIN11	-	
K11	AIN12	-	
L11	AIN13	-	
M11	AIN14	-	
M10	AIN15	-	
D10	LDO15_BYN	-	Connect to VSSA
C10	LDO28_BYN	-	Connect to VSSA
D12	DAC0	-	Float
C12	DAC1	-	Float
B12	CMP0A	-	Pulldown to 0V or VSS
A12	CMP0B	-	Pulldown to 0V or VSS
D11	CMP1A	-	Pulldown to 0V or VSS
C11	CMP1B	-	Pulldown to 0V or VSS
M4	CAN0_RXD	I	Pulldown to 0V or VSS
M5	CAN0_TXD	O	Float
M6	UART0_TXD	O	Float
M7	UART0_RXD	I	Pulldown to 0V or VSS
A2	CAN1_RXD/GPIO32	I	Pulldown to 0V or VSS
B1	CAN1_TXD/GPIO33	O	Float
C1	UART1_TXD/GPIO34	O	Float
D1	UART1_RXD/GPIO35	I	Pulldown to 0V or VSS
E1	SCL0/GPIO36	I	Pulldown to 0V or VSS

Pin No.	NAME	RESET STATE	Recommended Termination when Unused
F1	SDA0/GPIO37	I	Pulldown to 0V or VSS
J1	SCL1/GPIO38	I	Pulldown to 0V or VSS
K1	SDA1/GPIO39	I	Pulldown to 0V or VSS
L1	SCLK/GPIO40	I	Pulldown to 0V or VSS
M1	MOSI/GPIO41	O	Float
M2	MISO/GPIO42	I	Pulldown to 0V or VSS
M3	SSN0/GPIO43	O	Float
B2	PWM0/GPIO44	O	Float
B3	PWM1/GPIO45	O	Float
C2	PWM2/GPIO46	O	Float
C3	PWM3/GPIO47	O	Float
D2	GPIO0	I	Pulldown to 0V or VSS
D3	GPIO1	I	Pulldown to 0V or VSS
E2	GPIO2	I	Pulldown to 0V or VSS
E3	GPIO3	I	Pulldown to 0V or VSS
F2	GPIO4	I	Pulldown to 0V or VSS
F3	GPIO5	I	Pulldown to 0V or VSS
G2	GPIO6	I	Pulldown to 0V or VSS
G3	GPIO7	I	Pulldown to 0V or VSS
H2	GPIO8	I	Pulldown to 0V or VSS
H3	GPIO9	I	Pulldown to 0V or VSS
J2	GPIO10	I	Pulldown to 0V or VSS
J3	GPIO11	I	Pulldown to 0V or VSS

Pin No.	NAME	RESET STATE	Recommended Termination when Unused
K2	GPIO12	I	Pulldown to 0V or VSS
K3	GPIO13	I	Pulldown to 0V or VSS
L2	GPIO14	I	Pulldown to 0V or VSS
L3	GPIO15	I	Pulldown to 0V or VSS
B10	GPIO16/INTR17	I	Pulldown to 0V or VSS
B9	GPIO17/INTR18	I	Pulldown to 0V or VSS
B8	GPIO18/INTR19	I	Pulldown to 0V or VSS
B7	GPIO19/INTR20	I	Pulldown to 0V or VSS
B6	GPIO20/INTR21	I	Pulldown to 0V or VSS
B5	GPIO21/INTR22	I	Pulldown to 0V or VSS
B4	GPIO22/INTR23	I	Pulldown to 0V or VSS
A3	GPIO23/INTR24	I	Pulldown to 0V or VSS
A11	GPIO24/CMP0OUT	I	Float
A10	GPIO25/CMP1OUT	I	Float
A9	GPIO26/RTCK	I	Float
A8	GPIO27/SSN1	I	Pulldown to 0V or VSS
A7	GPIO28/SSN2	I	Pulldown to 0V or VSS
A6	GPIO29/SSNM	I	Pulldown to 0V or VSS
A5	GPIO30	I	Pulldown to 0V or VSS
A4	GPIO31	I	Pulldown to 0V or VSS
H1	XTAL1	I	Pulldown to 0V or VSS
G1	XTAL2	O	Float
K4	TRST	-	Pulldown to 0V or VSS
L7	TMS	-	Pulldown to 0V or VSS

Pin No.	NAME	RESET STATE	Recommended Termination when Unused
L6	TCK	-	Pulldown to 0V or VSS
K5	TDI	-	Pulldown to 0V or VSS
L4	TDO	-	Float

2.0 REVISION HISTORY

Date	Rev. #	Change Description
Mar 2018	1.0.0	Initial Release



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